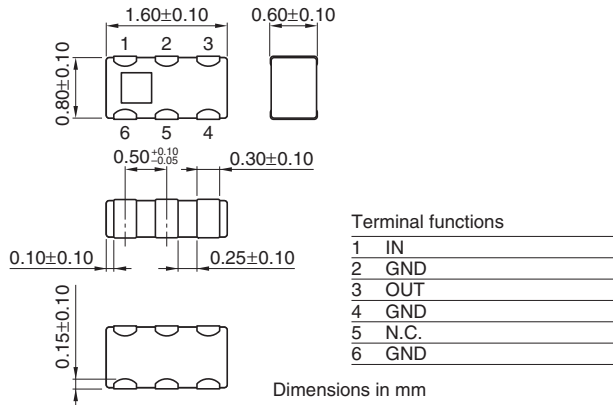


Multilayer Chip Low Pass Filters For LTE Band13

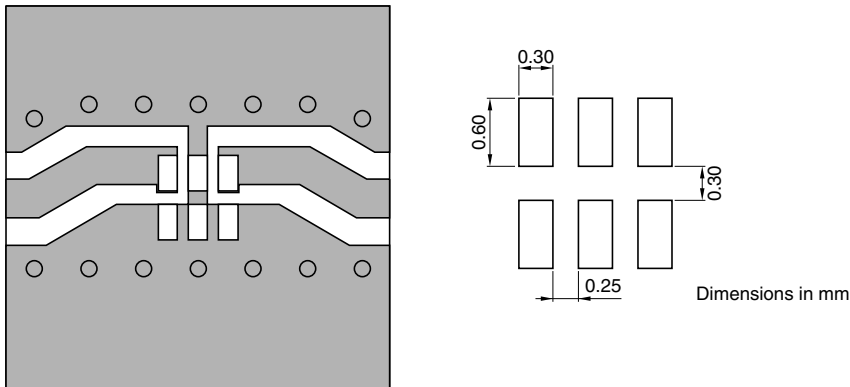
Conformity to RoHS Directive

DEA Series DEA160787LT-5032A1

SHAPES AND DIMENSIONS



RECOMMENDED PC BOARD PATTERNS



Line width to be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

ELECTRICAL CHARACTERISTICS

Item			Minimum value	Typical value	Maximum value	
Center frequency	[628.50MHz]		—	—	—	
Insertion loss	[-40 to +85°C]	[470 to 787MHz]	(dB)	0.46	0.65	
		[470 to 787MHz]	(dB)	—	-	0.71
VSWR	[470 to 787MHz]	[470 to 787MHz]	—	1.14	1.5	
		[1429 to 1501MHz]	(dB)	26	39.7	—
		[1565 to 1570MHz]	(dB)	30	46.5	—
		[1570 to 1580MHz]	(dB)	35	46.8	—
		[1580 to 1607MHz]	(dB)	30	46.9	—
Power	—	[1920 to 1980MHz]	(dB)	20	32.4	
			(W)	—	—	3.0
Temperature range	Operating		(°C)	-40	+85	
		Storage	(°C)	-40	—	+85

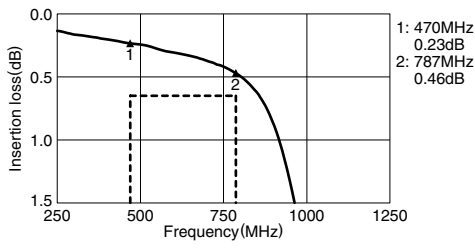
• Ta: +25±5°C

• Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

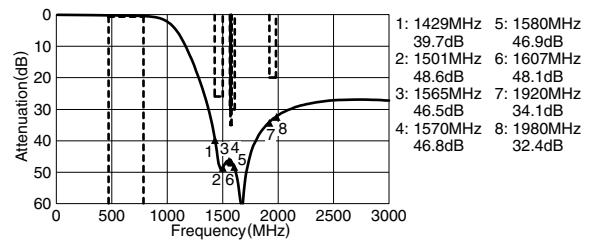
• All specifications are subject to change without notice.

FREQUENCY CHARACTERISTICS

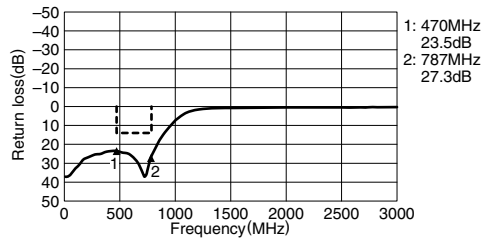
INSERTION LOSS



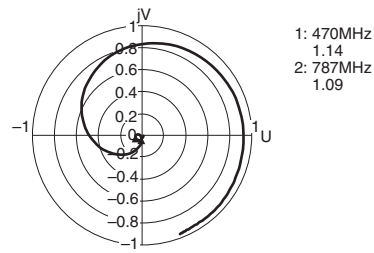
ATTENUATION



RETURN LOSS



VSWR



• All specifications are subject to change without notice.